

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10086718	03/04/2002	324	76.12	2858	Vincent Q. Nguyen

**APPLICANTS: Miyagawa Hirokazu; Yamamoto Katsumi; Matsuura Tatsuji; Kokubo Masaru;

**CONTINUING DATA VERIFIED: None/V.N

** FOREIGN APPLICATIONS VERIFIED: Yes/V.N
JAPAN 2001-158823 05/28/2001

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no
35 USC 119 conditions met ☐ yes ☐ no
Verified and Acknowledged Examiners's initials *V. Nguyen*

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H-1031

TITLE : Modulation semiconductor integrated circuit device and testing method for oscillation circuit

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for 0.8
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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